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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Date Submitted: April 3, 2009

(use as many sheets as necessary)

Sheet

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of

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Complete if Known

Application Number	09/896,797
Filing Date	6/29/2001
First Named Inventor	John Trezza
Art Unit	2613
Examiner Name	Agustin Bello
Attorney Docket Number	088245-4667

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		4,990,803	02-05-1991	Gilbert, Barrie	
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(use as many sheets as necessary)			First Named Inventor	John Trezza
Sheet	2	of	Art Unit	2613
			Examiner Name	Agustin Bello
			Attorney Docket Number	088245-4667

FOREIGN PATENT DOCUMENTS						
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		JP 09-203822	08-05-1997	Kyocera Corp.		
		EP 1 065 059	01-03-2001	Canon Kabushiki Kaisha		
		JP 2002-162536	06-07-2002	YKK Corp.		
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		EP 0 768 546	04-16-1997	Furukawa Electric Co. Ltd.		
		EP 0 889 338	01-07-1999	The Whitaker corp.		
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		WO 98/57208	12-17-1998	Minnesota Mining and Manufacturing Company		

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.				
		Communication for EP application Serial No. 02 759 141 dated November 21, 2005.				
		Communication for EP application Serial No. 02 763 264 dated November 9, 2005.				
		Copy of English translation of first Office Action issued in corresponding Chinese Application No. 2004800321558 dispatched on February 6, 2009.				
		Copy of English translation of Office Action issued in corresponding Korean Application No. 10-2003-7016814 issued November 5, 2008.				
		Copy of International Preliminary Examination Report for PCT/US02/21950 completed June 9, 2003.				
		Copy of International Preliminary Examination Report for PCT/US02/20830 completed April 9, 2003.				

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		Copy of International Preliminary Examination Report for PCT/US02/22053 completed May 13, 2003.
		Copy of International Preliminary Examination Report for PCT/US02/22084 completed April 16, 2003.
		Copy of International Preliminary Examination Report for PCT/US02/22090 completed April 17, 2003.
		Copy of International Preliminary Examination Report for PCT/US02/22092 completed September 20, 2006.
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		Copy of International Search Report for PCT/US02/22084 completed April 16, 2003.

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		Copy of International Search Report for PCT/US03/06322 mailed July 28, 2003.
		Copy of International Search Report for PCT/US03/22332 mailed March 22, 2004.
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		De Brabander et al., "Single Polarization Optical Waveguide on Silicon," IEEE Journal of Quantum Electronics, v. 27, No. 3 (March 1, 1991), pp. 575-579.
		Dines, "Smart Pixel Optoelectronic Receiver Based on a Charge Sensitive Amplifier Design," IEEE Journal of Selected topics in Quantum Electronics, V. 2, No. 1 (April 1996), pp. 117-120.
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		First Office Action for Chinese application Serial No. 028130995 dispatched on March 25, 2005.
		Madhavet et al., "Low-power 2.5Gbit/s VCSEL Driver in 0.5um CMOS Technology," Electronics Letters, V. 34, No. 2 (Jan. 22, 1998), pp. 178-179.
		Supplementary European Search Report for EP application Serial No. 02 76 3264 completed June 16, 2005.
		Supplementary Partial European Search Report and Supplementary European Search Report for EP application Serial No. 02 759 141 completed on June 27, 2005.
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		Van Blerkom, "Transimpedance Receiver Design Optimization for Smart Pixel Arrays," Journal of Lightwave Technology, V. 16, No. 1 (Jan. 1998), pp. 119-126.
		Woodward et al., "Optical Receivers for Optoelectronic VLSI," IEEE Journal of Selected Topics in Quantum Electronics, V. 2, No. 1 (April 1996), pp. 106-115.
		Woodward, "Optical Receivers for Smart Pixel Applications," Photonics West Conference, Jan. 1996, pp. 67-68.
		Woodward, et al., "Parallel Operation of 50 Element Two-Dimensional CMOS Smart-Pixel Receiver Array," IEEE Electronic Letters, Vol. 34, No. 10, (14 May 1998), pp. 936-37.

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